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SEP 1 2 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

N. MIYAMOTO, et al.

Application No.:

10/781,746

Filed:

February 20, 2004

For:

SEMICONDUCTOR INTEGRATED-CIRCUIT DEVICE AND

METHOD TO SPEED-UP CMOS CIRCUIT

Art Unit:

2819

Examiner:

Vibol TAN

PETITION FOR EXTENSION OF TIME

Commissioner For Patents P.O. Box 1450 Alexandria, VA 22313-1450 September 12, 2005

Sir.

In the matter of the above-Identified application, Applicants hereby respectfully petition for an extension of time to permit filing a response within a onemonth time period subsequent to expiration of the shortened statutory period set in the outstanding Office Action mailed May 12, 2005. A credit card payment form in the amount of \$120.00 to cover the required fee for the requested extension of time is attached hereto.

It is respectfully requested that any shortage in the fee be charged to the deposit account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135 (Docket No.520.43528X00) and any excess fees credited to that account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

Gregory E. Montone Reg. No. 28,141

GEM/gjb

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